


# CERTIFICATE OF CONFORMITY

Issued to: Huawei Technologies Co., Ltd.  
Administration Building, Headquarters of Huawei Technologies Co., Ltd., Bantian,  
Longgang District, Shenzhen, Guangdong, 518129, China

For the product: SOLAR INVERTER

Trade name:   
**HUAWEI**

Type/Model: SUN2000-100KTL-M2, SUN2000-115KTL-M2

Ratings: SUN2000-100KTL-M2:  
PV input: Max 1100 Vdc, MPPT voltage range: 200-1000 Vdc, Isc PV: 40 A × 10,  
Max 30 A × 10  
AC output: 3/N/PE, 400 Vac, 50Hz, rated 144.4 A, 100 kW, max 160.4 A, Max 110 kVA  
3/PE, 480 Vac, 50Hz, rated 120.3 A, 100 kW, max 133.7 A, Max 110 kVA  
SUN2000-115KTL-M2:  
PV input: Max 1100 Vdc, MPPT voltage range: 200-1000 Vdc, Isc PV: 40 A × 10,  
Max 30 A × 10  
AC output: 3/N/PE, 400 Vac, 50 Hz, rated 166.0 A, 115 kW, max 182.3 A, Max 125 kVA  
3/PE, 480 Vac, 50Hz, rated 138.4 A, 115 kW, max 151.9 A, Max 125 kVA

Manufactured by: Huawei Technologies Co., Ltd.  
Administration Building, Headquarters of Huawei Technologies Co., Ltd., Bantian,  
Longgang District, Shenzhen, Guangdong, 518129, China

Requirements: Engineering Recommendation G99 Issue 1 – Amendment 6:2020 (G99/1-6:2020)  
(Requirements for Type A and B Generating Module)

This Test Certificate is granted on account of an examination by DEKRA, the results of which are laid down in a confidential file no. 6139156.50

The examination has been carried out on one single specimen or several specimens of the product, submitted by the manufacturer. The certificate does not include an assessment of the manufacturer's production. Conformity of his production with the specimen tested by DEKRA is not the responsibility of DEKRA.

Shanghai, 19 December 2022 Certificate Number: 6139156.01COC  
It expires at the latest on: 19 December 2027

DEKRA Testing and Certification (Shanghai) Ltd.



Cliff Lin  
Certification Manager

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Accreditation of the certification body by IAS according to ISO/IEC 17065 for products.  
Accreditation is valid in the areas of certification mentioned in the certificate.

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# Annex

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**G99/1-6 Compliance Verification Report –Tests for Inverter Connected Power Generating Modules**

**Extract form test report number.: 6139156.50**

**Operating Range**

**P**

**Operating Range:** Five tests should be carried with the **Power Generating Module operating at Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within  $\pm 5\%$  of the apparent power value set for the entire duration of each test sequence.

Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

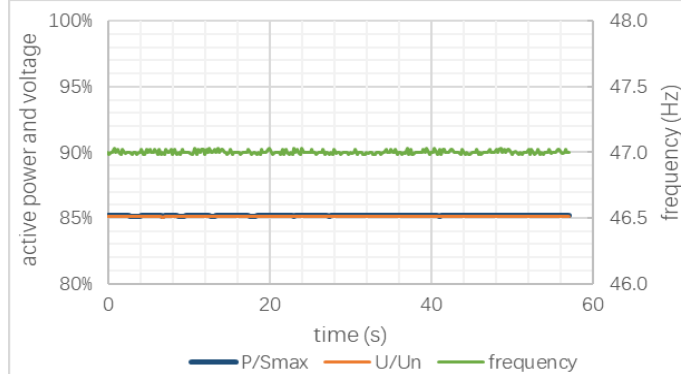
The **Interface Protection** shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover **Inverter/rectifier** may be replaced by a DC source.

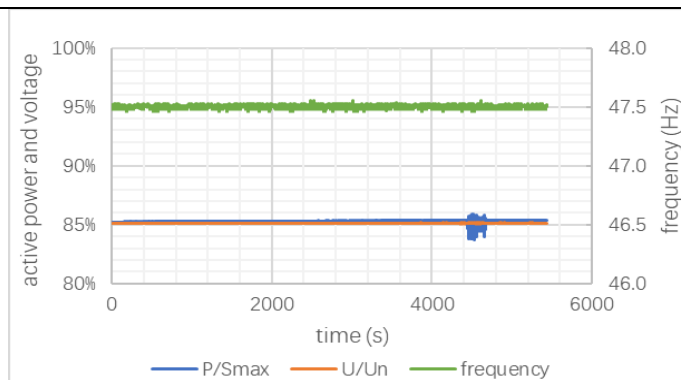
**Test 1**

Voltage = 85% of nominal (195.5 V),  
Frequency = 47 Hz,  
**Power Factor** = 1,  
Period of test 20 s



**Test 2**

Voltage = 85% of nominal (195.5 V),  
Frequency = 47.5 Hz,  
**Power Factor** = 1,  
Period of test 90 minutes



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<p>Test 3 Voltage = 110% of nominal (253 V), Frequency = 51.5 Hz, <b>Power Factor</b> = 1, Period of test 90 minutes</p>		
<p>Test 4 Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, <b>Power Factor</b> = 1, Period of test 15 minutes</p>		
<p>Test 5 Voltage = 100% of nominal (230 V), Frequency = 50.0 Hz, <b>Power Factor</b> = 1, Period of test = 90 minutes</p>		
<p>Test 6 RoCoF withstand Confirm that the <b>Power Generating Module</b> is capable of staying connected to the <b>Distribution Network</b> and operate at rates of change of frequency up to 1 Hz/s as measured over a period of 500 ms. Note that this is not expected to be demonstrated on site.</p>		
<p>Model: SUN2000-115KTL-M2</p>		<p>P</p>
<p>Test 1:</p>		

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Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (seconds)
195.73	47.00	106509.4	1.000	> 20
<b>Test 2:</b>				
Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (Minutes)
195.83	47.50	106701.3	1.000	> 90
<b>Test 3:</b>				
Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (Minutes)
253.81	51.50	115535.8	1.000	> 90
<b>Test 4:</b>				
Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (Minutes)
253.82	52.00	115547.1	1.000	> 15
<b>Test 5:</b>				
Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (Minutes)
230.93	50.00	126416.7	1.000	> 90
<b>Test 6:</b>				
Measured Voltage (V)	Ramp range	Test frequency ramp	Test Duration	Confirm no trip
195.5	47.0 Hz to 52.0 Hz	+1 Hzs <sup>-1</sup>	5.0 s	no trip
253.0	52.0 Hz to 49.0 Hz	-1 Hzs <sup>-1</sup>	3.1 s	no trip

<b>Power Quality – Harmonics:</b>				<b>P</b>
<p>For <b>Power Generating Modules</b> of <b>Registered Capacity</b> of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12 The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 61000-3-12 for three phase equipment.</p> <p><b>Power Generating Modules</b> with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the <b>Power Generating Module</b> in order to accept the connection to a <b>Distribution Network</b>.</p> <p>For <b>Power Generating Modules</b> of <b>Registered Capacity</b> of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC G5.</p>				
Model: SUN2000-115KTL-M2				
<b>Power Generating Module</b> tested to BS EN 61000-3-12				
<b>Power Generating Module</b> rating per phase (rpp)	41.67	kVA	Harmonic % = Measured Value (A) x 23/rating per phase (kVA)	
Single or three phase measurements (for single phase measurements, only complete L1 columns below)	three phase PV inverter			

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Harmonic	At 45-55% of <b>Registered Capacity</b>						Limit in BS EN 61000-3-12	
	Measured Value (MV) in Amps			Measured Value (MV) in %			1 phase	3 phase
	L1	L2	L3	L1	L2	L3		
2	0.080	0.199	0.193	0.048	0.120	0.116	8%	8%
3	0.183	0.126	0.146	0.110	0.076	0.088	21.6%	Not stated
4	0.136	0.073	0.101	0.082	0.044	0.061	4%	4%
5	0.033	0.044	0.045	0.020	0.026	0.027	10.7%	10.7%
6	0.038	0.039	0.066	0.023	0.024	0.039	2.67%	2.67%
7	0.040	0.049	0.041	0.024	0.030	0.025	7.2%	7.2%
8	0.036	0.037	0.050	0.022	0.022	0.030	2%	2%
9	0.057	0.057	0.059	0.034	0.034	0.035	3.8%	Not stated
10	0.046	0.039	0.053	0.028	0.023	0.032	1.6%	1.6%
11	0.088	0.065	0.065	0.053	0.039	0.039	3.1%	3.1%
12	0.045	0.046	0.058	0.027	0.028	0.035	1.33%	1.33%
13	0.050	0.100	0.080	0.030	0.060	0.048	2%	2%
THD	-	-	-	0.17	0.18	0.19	23%	13%
PWHD	-	-	-	0.39	0.40	0.42	23%	22%
Harmonic	At 100% of <b>Registered Capacity</b>						Limit in BS EN 61000-3-12	
	Measured Value (MV) in Amps			Measured Value (MV) in %			1 phase	3 phase
	L1	L2	L3	L1	L2	L3		
2	0.200	0.233	0.307	0.120	0.140	0.185	8%	8%
3	0.480	0.681	0.415	0.289	0.410	0.250	21.6%	Not stated
4	0.112	0.070	0.139	0.067	0.042	0.084	4%	4%
5	0.108	0.110	0.117	0.065	0.066	0.070	10.7%	10.7%
6	0.066	0.053	0.065	0.040	0.032	0.039	2.67%	2.67%
7	0.216	0.158	0.155	0.130	0.095	0.093	7.2%	7.2%
8	0.046	0.049	0.055	0.028	0.029	0.033	2%	2%
9	0.121	0.069	0.146	0.073	0.041	0.088	3.8%	Not stated
10	0.066	0.051	0.049	0.040	0.031	0.030	1.6%	1.6%
11	0.129	0.129	0.077	0.078	0.078	0.046	3.1%	3.1%
12	0.053	0.061	0.058	0.032	0.037	0.035	1.33%	1.33%
13	0.109	0.127	0.094	0.066	0.076	0.057	2%	2%
THD	-	-	-	0.38	0.47	0.37	23%	13%
PWHD	-	-	-	0.81	0.91	0.74	23%	22%

Model: SUN2000-100KTL-M2

**Power Generating Module** tested to BS EN 61000-3-12

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<b>Power Generating Module</b> rating per phase (rpp)				36.67		kVA		Harmonic % = Measured Value (A) x 23/rating per phase (kVA)	
Single or three phase measurements (for single phase measurements, only complete L1 columns below)				three phase PV inverter					
Harmonic	At 45-55% of <b>Registered Capacity</b>						Limit in BS EN 61000-3-12		
	Measured Value (MV) in Amps			Measured Value (MV) in %					
	L1	L2	L3	L1	L2	L3	1 phase	3 phase	
2	0.059	0.083	0.079	0.041	0.058	0.055	8%	8%	
3	0.124	0.098	0.132	0.086	0.068	0.091	21.6%	Not stated	
4	0.057	0.052	0.040	0.039	0.036	0.028	4%	4%	
5	0.046	0.048	0.054	0.032	0.033	0.037	10.7%	10.7%	
6	0.051	0.065	0.039	0.036	0.045	0.027	2.67%	2.67%	
7	0.054	0.058	0.050	0.037	0.040	0.034	7.2%	7.2%	
8	0.040	0.040	0.043	0.028	0.028	0.030	2%	2%	
9	0.058	0.068	0.060	0.040	0.047	0.041	3.8%	Not stated	
10	0.059	0.063	0.061	0.041	0.043	0.042	1.6%	1.6%	
11	0.075	0.111	0.081	0.052	0.077	0.056	3.1%	3.1%	
12	0.063	0.064	0.059	0.044	0.045	0.041	1.33%	1.33%	
13	0.068	0.062	0.079	0.047	0.043	0.055	2%	2%	
THD	-	-	-	0.16	0.17	0.17	23%	13%	
PWHD	-	-	-	0.42	0.46	0.43	23%	22%	
Harmonic	At 100% of <b>Registered Capacity</b>						Limit in BS EN 61000-3-12		
	Measured Value (MV) in Amps			Measured Value (MV) in %					
	L1	L2	L3	L1	L2	L3	1 phase	3 phase	
2	0.087	0.150	0.119	0.060	0.103	0.082	8%	8%	
3	0.370	0.349	0.233	0.255	0.241	0.161	21.6%	Not stated	
4	0.076	0.140	0.076	0.052	0.096	0.052	4%	4%	
5	0.116	0.045	0.109	0.080	0.031	0.075	10.7%	10.7%	
6	0.088	0.096	0.039	0.060	0.067	0.027	2.67%	2.67%	
7	0.170	0.176	0.161	0.118	0.121	0.111	7.2%	7.2%	
8	0.049	0.081	0.084	0.033	0.056	0.058	2%	2%	
9	0.075	0.165	0.105	0.052	0.114	0.073	3.8%	Not stated	
10	0.066	0.051	0.054	0.045	0.035	0.037	1.6%	1.6%	
11	0.169	0.086	0.149	0.116	0.059	0.103	3.1%	3.1%	
12	0.057	0.050	0.061	0.039	0.035	0.042	1.33%	1.33%	
13	0.047	0.185	0.178	0.033	0.128	0.123	2%	2%	
THD	-	-	-	0.34	0.37	0.30	23%	13%	

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PWHD	-	-	-	0.77	0.89	0.80	23%	22%
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1. THD = Total Harmonic Distortion

2. PWHD = Partial Weighted Harmonic Distortion

Power Quality – Voltage fluctuations and Flicker:									P
Model:		SUN2000-115KTL-M2							
Test start date		2022-08-11			Test end date			2022-10-21	
Test location		No.99, Hongye Road, Suzhou Industrial Park, Suzhou, Jiangsu, P.R. China							
		Starting			Stopping			Running	
		d(max)	d(c)	d(t)	d(max)	d(c)	d(t)	P <sub>st</sub>	P <sub>It</sub> 2 hours
Measured Values at test impedance	L1	0.45	0.16	0	1.11	0.18	0	0.25	0.22
	L2	0.60	0.15	0	1.04	0.20	0	0.25	0.23
	L3	0.53	0.16	0	1.00	0.29	0	0.25	0.23
Normalised to standard impedance		4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65
Normalised to required maximum impedance		-	-	-	-	-	-	-	-
Limits set under BS EN 61000-3-11		4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65
Test Impedance	R	0.24	Ω	XI	0.15	Ω			
Standard Impedance	R	0.24 * 0.4 ^	Ω	XI	0.15 * 0.25 ^	Ω			
Maximum Impedance	R	N/A	Ω	XI	N/A	Ω			
* Applies to three phase and split single phase <b>Power Generating Modules</b> .									
^ Applies to single phase <b>Power Generating Module</b> and <b>Power Generating Modules</b> using two phases on a three phase system. Delete as appropriate.									

Power Quality – Voltage fluctuations and Flicker:									P
Model:		SUN2000-100KTL-M2							
Test start date		2022-08-12			Test end date			2022-10-24	
Test location		No.99, Hongye Road, Suzhou Industrial Park, Suzhou, Jiangsu, P.R. China							
		Starting			Stopping			Running	
		d(max)	d(c)	d(t)	d(max)	d(c)	d(t)	P <sub>st</sub>	P <sub>It</sub> 2 hours
Measured Values at test impedance	L1	0.76	0.18	0	0.57	0.17	0	0.14	0.14
	L2	0.64	0.17	0	0.53	0.19	0	0.17	0.16
	L3	0.63	0.23	0	0.66	0.28	0	0.17	0.17
Normalised to		4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65

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standard impedance								
Normalised to required maximum impedance	-	-	-	-	-	-	-	-
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65
Test Impedance	R	0.24	$\Omega$	XI	0.15	$\Omega$		
Standard Impedance	R	0.24 * 0.4 ^	$\Omega$	XI	0.15 * 0.25 ^	$\Omega$		
Maximum Impedance	R	N/A	$\Omega$	XI	N/A	$\Omega$		
* Applies to three phase and split single phase <b>Power Generating Modules</b> .								
^ Applies to single phase <b>Power Generating Module</b> and <b>Power Generating Modules</b> using two phases on a three phase system. Delete as appropriate.								

<b>4. Power quality – DC injection:</b>					<b>P</b>
Model: SUN2000-115KTL-M2					
Test power level		10%	55%	100%	
Recorded DC injection value (mA)	L1	-120	-120	-70	
	L2	-200	-190	-150	
	L3	-240	-250	-220	
as % of rated AC current	L1	0.072	0.072	0.042	
	L2	0.120	0.114	0.090	
	L3	0.145	0.151	0.133	
Limit		0.25%	0.25%	0.25%	
Model: SUN2000-100KTL-M2					
Test power level		10%	55%	100%	
Recorded DC injection value(mA)	L1	-90	-100	40	
	L2	-140	-180	70	
	L3	-190	-240	60	
as % of rated AC current	L1	-0.062	-0.069	0.028	
	L2	-0.097	-0.125	0.048	
	L3	-0.132	-0.166	0.042	
Limit		0.25%	0.25%	0.25%	

<b>Power Factor:</b>				<b>P</b>
Model: SUN2000-115KTL-M2				
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)	



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Measured value	1.000	1.000	1.000
<b>Power Factor Limit</b>	>0.95	>0.95	>0.95
Model: SUN2000-1100KTL-M2			
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	1.000	1.000	1.000
<b>Power Factor Limit</b>	>0.95	>0.95	>0.95

Protection – Frequency tests:						P
Model: SUN2000-115KTL-M2						
Function	Setting		Trip test		“No trip tests”	
	Frequency	Time delay	Frequency	Time delay	Frequency / time	Confirm no trip
U/F stage 1	47.5 Hz	20 s	47.40 Hz	20.02 s	47.7 Hz 30 s	no trip
U/F stage 2	47.0 Hz	0.5 s	47.00 Hz	0.540 s	47.2 Hz 19.5 s	no trip
					46.8 Hz 0.45 s	no trip
O/F	52.0 Hz	0.5 s	52.01 Hz	0.532 s	51.8 Hz 120.0 s	no trip
					52.2 Hz 0.45 s	no trip

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Protection – Voltage tests (LV Protection)						P
Model: SUN2000-115KTL-M2						
Phase L1						
Function	Setting		Trip test		“No trip tests”	
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V	0.8 pu (184 V)	2.5 s	184.43 V	2.530 s	188 V 5.0 s	no trip
					180 V 2.45 s	no trip
O/V stage 1	1.14 pu (262.2 V)	1.0 s	263.88 V	1.038 s	258.2 V 5.0 s	no trip
O/V stage 2	1.19 pu (273.7 V)	0.5 s	273.01 V	0.510 s	269.7 V 0.95 s	no trip
					277.7 V 0.45 s	no trip
Phase L2						
Function	Setting		Trip test		“No trip tests”	
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V	0.8 pu (184 V)	2.5 s	184.35 V	2.525 s	188 V 5.0 s	no trip
					180 V 2.45 s	no trip
O/V stage 1	1.14 pu (262.2 V)	1.0 s	263.90 V	1.028 s	258.2 V 5.0 s	no trip
O/V stage 2	1.19 pu (273.7 V)	0.5 s	273.71 V	0.526 s	269.7 V 0.95 s	no trip
					277.7 V 0.45 s	no trip
Phase L3						
Function	Setting		Trip test		“No trip tests”	
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V	0.8 pu (184 V)	2.5 s	184.24 V	2.515 s	188 V 5.0 s	no trip
					180 V 2.45 s	no trip
O/V stage 1	1.14 pu (262.2 V)	1.0 s	262.93 V	1.034 s	258.2 V 5.0 s	no trip
O/V stage 2	1.19 pu (273.7 V)	0.5 s	273.41 V	0.522 s	269.7 V 0.95 s	no trip
					277.7 V 0.45 s	no trip

Protection – Voltage tests (HV Protection)						P
Model: SUN2000-115KTL-M2						

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L1 Phase						
Function	Setting		Trip test		"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V	0.8 pu (184 V)	2.5s	184.43 V	2.532 s	188 V 5.0 s	No trip
					180 V 2.45 s	No trip
O/V stage 1	1.10 pu (253 V)	1.0s	253.39 V	1.029 s	249 V 5.0 s	No trip
O/V stage 2	1.13 pu (259.9 V)	0.5s	260.07 V	0.525 s	254.9 V 0.95 s	No trip
					263.9 V 0.45 s	No trip
L2 Phase						
Function	Setting		Trip test		"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage/time	Confirm no trip
U/V	0.8 pu (184 V)	2.5s	184.09 V	2.532 s	188 V 5.0 s	No trip
					180 V 2.45 s	No trip
O/V stage 1	1.10 pu (253 V)	1.0s	253.36 V	1.029 s	249 V 5.0 s	No trip
O/V stage 2	1.13 pu (259.9 V)	0.5s	259.96 V	0.525 s	254.9 V 0.95 s	No trip
					263.9 V 0.45 s	No trip
L3 Phase						
Function	Setting		Trip test		"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage/time	Confirm no trip
U/V	0.8 pu (184 V)	2.5s	184.24 V	2.532 s	188 V 5.0 s	No trip
					180 V 2.45 s	No trip
O/V stage 1	1.10 pu (253 V)	1.0s	253.12 V	1.037 s	249 V 5.0 s	No trip
O/V stage 2	1.13 pu (259.9 V)	0.5s	260.09 V	0.526 s	254.9 V 0.95 s	No trip
					263.9 V 0.45 s	No trip

**Protection – Loss of Mains test:**

**P**

These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.

For test condition A, EUT output = 100 % P<sub>n</sub>, test condition B, EUT output = 50 % to 66 % P<sub>n</sub>, and test condition C, EUT output = 25 % to 33 % P<sub>n</sub>.

Model: SUN2000-115KTL-M2

The following sub set of tests should be recorded in the following table.

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Test Power and imbalance	33% -5% Q Test 22	66% -5% Q Test 12	100% -5% P Test 5	33% +5% Q Test 31	66% +5% Q Test 21	100% +5% P Test 10
Trip time. Limit is 0.5s	212.06 ms	195.26 ms	215.55 ms	201.56 ms	200.43 ms	331.69 ms

Loss of Mains Protection, Vector Shift Stability test				P
This test should be carried out in accordance with Annex A.7.1.2.6.				
Model: SUN2000-115KTL-M2				
Vector Shift	Start Frequency	Change	Confirm no trip	
Positive Vector Shift	49.5 Hz	+50 degrees	No trip	
Negative Vector Shift	50.5 Hz	- 50 degrees	No trip	

Loss of Mains Protection, RoCoF Stability test				P
This test should be carried out in accordance with Annex A.7.1.2.6.				
Model: SUN2000-115KTL-M2				
Ramp range	Test frequency ramp:	Test Duration	Confirm no trip	
49.0 Hz to 51.0 Hz	+0.95 Hzs <sup>-1</sup>	2.2 s	No trip	
51.0 Hz to 49.0 Hz	-0.95 Hzs <sup>-1</sup>	2.2 s	No trip	

TABLE: Reduction of active power on setpoint												P
Model	SUN2000-115KTL-M2											
1-min mean value, P/P <sub>n</sub> [%]	100	90	80	70	60	50	40	30	20	10	0	
P <sub>Setpoint</sub> [kW]:	115	103.5	92	80.5	69	57.5	46	34.5	23	11.5	0	
P <sub>E60</sub> [kW]:	116.2 9	103.6 7	92.18	80.64	69.12	57.56	46.05	34.49	23.01	11.47	2.25	
ΔP <sub>E60</sub> /P <sub>E<sub>max</sub></sub> [%]:	1.12	0.15	0.16	0.13	0.10	0.05	0.05	-0.01	0.01	-0.03	1.96	
Limit of reduce time [s]	5s											

Limited Frequency Sensitive Mode – Over frequency test		P
The test should be carried out using the specific threshold frequency of 50.4 Hz and <b>Drop</b> of 10%.		
Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex B.6.2	Yes	

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Model: SUN2000-115KTL-M2					
Test sequence at <b>Registered Capacity &gt;80%</b>	Measured <b>Active Power</b> Output (W)	Frequency (Hz)	Calculate droop (%)	Primary Power Source	<b>Active Power Gradient</b>
Step a) 50.00 Hz ±0.01 Hz	115310.0	50.00	--	Photovoltaic array simulator	-
Step b) 50.45 Hz ±0.05 Hz	114063.9	50.45	9.54		P
Step c) 50.70 Hz ±0.10 Hz	107813.0	50.70	9.26		P
Step d) 51.15 Hz ±0.05 Hz	96538.3	51.15	9.21		P
Step e) 50.70 Hz ±0.10 Hz	107808.0	50.70	9.25		P
Step f) 50.45 Hz ±0.05 Hz	114045.9	50.45	9.41		P
Step g) 50.00 Hz ±0.01 Hz	115279.0	50.00	--		-
Test sequence at <b>Registered Capacity 40% - 60%</b>	Measured <b>Active Power</b> Output (W)	Frequency (Hz)	Calculate droop (%)	Primary Power Source	<b>Active Power Gradient</b>
Step a) 50.00 Hz ±0.01 Hz	57558.3	50.00	--	Photovoltaic array simulator	-
Step b) 50.45 Hz ±0.05 Hz	56331.9	50.45	9.79		P
Step c) 50.70 Hz ±0.10 Hz	50087.6	50.70	9.29		P
Step d) 51.15 Hz ±0.05 Hz	38854.9	51.15	9.24		P
Step e) 50.70 Hz ±0.10 Hz	49975.8	50.70	9.15		P
Step f) 50.45 Hz ±0.05 Hz	56266.2	50.45	9.21		P
Step g) 50.00 Hz ±0.01 Hz	57556.5	50.00	--		-
<b>Note:</b> The resulting overall tolerance range for a nominal 10% Droop is +2.8% and – 1.5%, ie a Droop less than 12.8% and greater than 8.5%.					

<b>Power output with falling frequency test</b>					<b>P</b>
Tests should prove that the <b>Power Generating Module</b> does not reduce output power as the frequency falls. These tests should be carried out in accordance with 11.2.3.1, 12.2.3.1					
Model: SUN2000-115KTL-M2					
Test sequence	Measured <b>Active Power</b> Output (W)	Acceptable <b>Active Power</b>	Frequency (Hz)	Primary power source	
50.5 Hz for 5 minutes	114821.4	100% <b>Registered Capacity</b>	50.50	Photovoltaic array simulator	
50.0 Hz for 5 minutes	114834.4	100% <b>Registered Capacity</b>	50.00	Photovoltaic array simulator	
49.5 Hz for 5 minutes	114859.2	100% <b>Registered Capacity</b>	49.00	Photovoltaic array simulator	
49.0 Hz for 5 minutes	114857.2	99% <b>Registered Capacity</b>	48.00	Photovoltaic array simulator	
48.0 Hz for 5 minutes	114878.7	97% <b>Registered Capacity</b>	47.60	Photovoltaic array simulator	
47.6 Hz for 5 minutes	114858.1	96.2% <b>Registered Capacity</b>	47.10	Photovoltaic array simulator	
47.1 Hz for 20 s	114821.4	95% <b>Registered Capacity</b>	50.50	Photovoltaic array simulator	

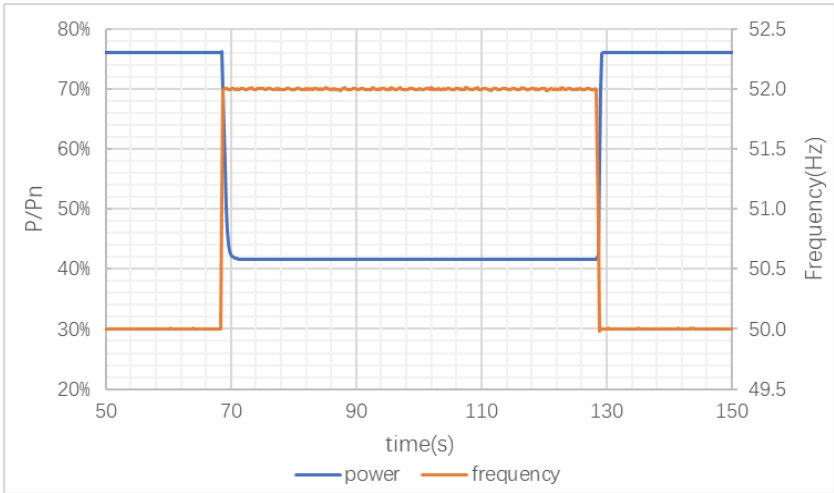
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Frequency Response Tests					P
<b>Active Power</b> response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex B.6.2.					
Model: SUN2000-115KTL-M2					
Test sequence at Registered Capacity >65%	Measured Active Power Output (kW)	Frequency (Hz)	Calculate droop (%)	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	87.55	50.00	--	Photovoltaic array simulator	-
Step b) 52.00 Hz ±0.01 Hz	47.93	52.00	9.29		P
Step c) 50.00 Hz ±0.01 Hz	87.55	50.00	--		-
Test sequence at Registered Capacity >80%	Measured Active Power Output (kW)	Frequency (Hz)	$\Delta P_{E60}/P_n$ (%)	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	115.24	50.00	--	Photovoltaic array simulator	-
Step b) 50.60 Hz ±0.01 Hz	110.17	50.60	9.11		P
Step c) 50.00 Hz ±0.01 Hz	115.25	50.00	--		-

**Frequency Response Test at Registered Capacity >65%**

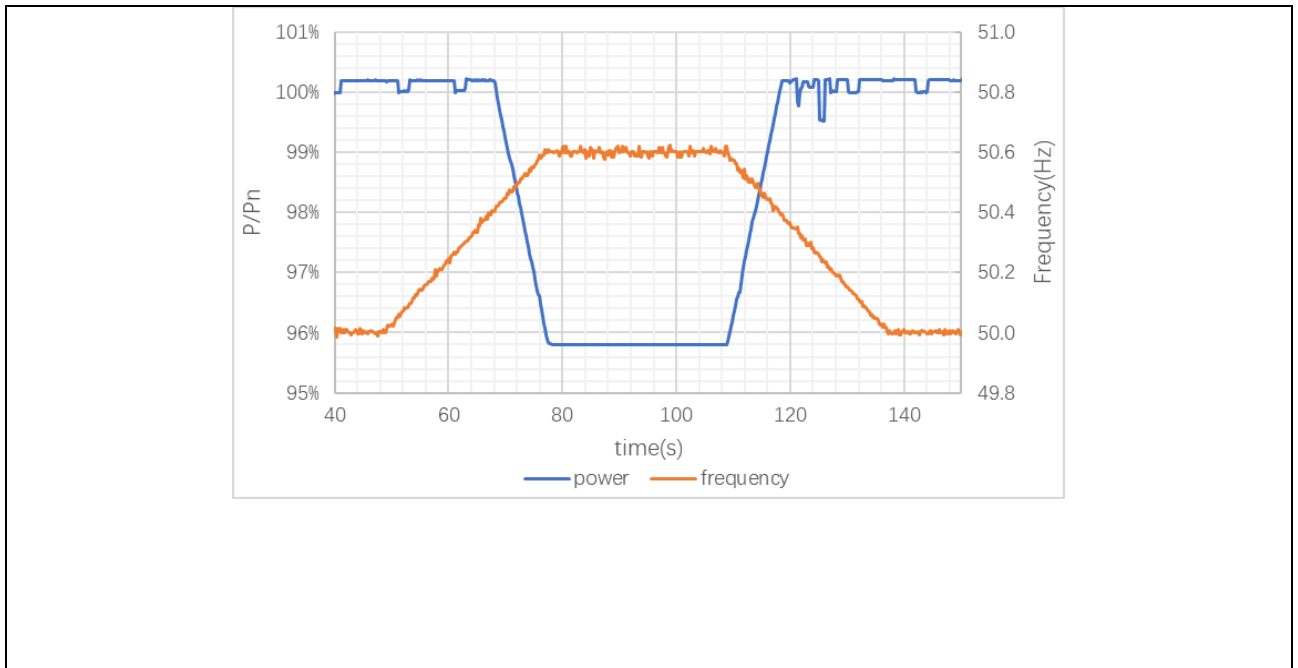


**Frequency Response Test at Registered Capacity >80%**

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Protection – Re-connection timer.					P
Model: SUN2000-115KTL-M2					
Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1.					
Time delay setting	Measured delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1.			
60 s	68 s	At 1.16 pu (266.2 V)	At 0.78 pu (180.0 V)	At 47.4 Hz	At 52.1 Hz
Confirmation that the <b>generator</b> does not re-connect.		No reconnection	No reconnection	No reconnection	No reconnection

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Reactive Capability					P
Model: SUN2000-115KTL-M2					
Inductive supply reactive power (nominal voltage)					
Rating power [%]	Active power [W]	Reactive Power [Var]	Q/Pn [%]	Power factor [cos φ]	Voltage [V]
10%	11470.6	3646.5	3.2%	0.953	230.5
20%	23061.8	7399.1	6.4%	0.952	230.7
30%	34557.0	11149.1	9.7%	0.952	230.6
40%	46113.8	14927.8	13.0%	0.951	230.4
50%	57621.7	18643.3	16.2%	0.951	230.6
60%	69151.2	22422.0	19.5%	0.951	230.1
70%	80667.1	26114.6	22.7%	0.951	230.4
80%	92206.9	29818.1	25.9%	0.951	230.8
90%	103724.0	33462.0	29.1%	0.952	230.7
100%	115276.7	36983.2	32.2%	0.952	230.7
Capacitive supply reactive power (nominal voltage)					
Rating power [%]	Active power [W]	Reactive Power [Var]	Q/Pn [%]	Power factor [cos φ]	Voltage [V]
10%	11466.7	-3889.2	-3.4%	0.947	229.9
20%	22993.2	-7705.3	-6.7%	0.948	230.2
30%	34485.0	-11480.5	-10.0%	0.949	230.3
40%	45991.4	-15270.5	-13.3%	0.949	230.7
50%	57490.8	-19065.9	-16.6%	0.949	230.4
60%	68973.5	-22878.6	-19.9%	0.949	230.5
70%	80483.7	-26723.8	-23.2%	0.949	230.6
80%	91957.8	-30609.5	-26.6%	0.949	230.3
90%	103421.8	-34537.1	-30.0%	0.949	230.7
100%	114784.3	-38582.5	-33.5%	0.948	230.3
Note:					
When supplying <b>Registered Capacity</b> all <b>Power Generating Modules</b> shall be capable of continuous operation at nominal voltage at any points between the limits of 0.95 <b>Power Factor</b> lagging and 0.95 <b>Power Factor</b> leading at the <b>Connection Point</b> or the <b>Generating Unit</b> terminals as appropriate for the <b>Power Generating Facility</b> and as agreed with the <b>DNO</b> .					



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<b>Fault level contribution:</b>		<b>P</b>
These tests shall be carried out in accordance with EREC G99 Annex A.7.1.5.		
For <b>Inverter</b> output		
Time after fault	Volts	Amps
20ms	8.68 V	110.77 A
100ms	4.23 V	50.60 A
250ms	3.02 V	32.79 A
500ms	2.32 V	23.99 A
Time to trip	6 ms	In seconds

<b>Self-Monitoring solid state switching:</b> No specified test requirements. Refer to Annex A.7.1.6.	
It has been verified that in the event of the solid state switching device failing to disconnect the <b>Power Park Module</b> , the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.	<b>Yes</b>
<b>Wiring functional tests: If required by para 15.2.1.</b>	
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	<b>Yes</b>
<b>Logic interface (input port).</b>	
Confirm that an input port is provided and can be used to shut down the module.	<b>Yes</b>
Additional comments.	
No.	

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